

column ribs and row ribs operable to define pixel cells in a column direction and in a row direction, respectively, to thereby define the pixel cells in a matrix; and

a' plane discharge electrodes having a display electrode portion and a bus electrode portion, wherein the display electrode portion comprises sustain electrodes and scan electrodes and the bus electrode portion comprises sustain-side bus electrodes and scan-side bus electrodes, and

wherein at least part of the display electrode portion has a notched portion or a cut-away portion between pixel cells adjacent to each other in the row direction, thereby providing each pixel cell with individually separated electrodes.

4. (Amended) A method for fabricating the plasma display panel of claim 1, comprising the steps of:

a² encapsulating said rear substrate and said front substrate in a vacuum, and sealing a discharge gas in the panel continually thereafter without exposing the interior of the panel to the atmosphere.

5. (Amended) The plasma display panel according to claim 1, wherein said column ribs and row ribs form lattice-shaped ribs and are provided on said rear substrate.

11. (Amended) The plasma display panel according to claim 6, further comprising horizontal barrier walls having a thickness of 2 to 50 μ m between pixel cells, said horizontal barrier walls being formed in parallel to the bus electrode portion.

12. (Amended) The plasma display panel according to claim 11, wherein said horizontal barrier wall is formed of a material having a dielectric constant lower than that of an insulating layer provided on said front substrate.

13. (Amended) The plasma display panel according to claim 11, wherein said horizontal barrier wall is placed only between the sustain-side bus electrodes.

14. (Amended) The plasma display panel according to claim 11, wherein said horizontal barrier walls between the sustain-side bus electrodes and between the scan-side bus electrodes have different widths.

15. (Amended) The plasma display panel according to claim 11, wherein the horizontal barrier walls are provided with an extended portion formed orthogonal to the longitudinal direction of the horizontal barrier wall, said extended portion being disposed between pixel cells adjacent to each other in the longitudinal row direction.

16. (Amended) The plasma display panel according to claim 6, wherein said column ribs and row ribs form lattice-shaped ribs and are provided on the rear substrate, wherein a rib portion extending in the longitudinal row direction for defining pixel cells is higher than a rib portion extending in the longitudinal column direction for defining pixel cells.

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17. (Amended) The plasma display panel according to claim 11, wherein a pair of sustain-side bus electrodes or scan-side bus electrodes are not overlapped by the horizontal barrier but are overlapped by each of said ribs.

18. (Amended) The plasma display panel according to claim 11, wherein a pair of sustain-side bus electrodes or scan-side bus electrodes is not overlapped by each of said ribs but is overlapped by the horizontal barrier.

19. (Amended) The plasma display panel according to claim 11, wherein each of said ribs and the horizontal barrier overlap a pair of sustain-side bus electrodes or scan-side bus electrodes.

20. (Amended) The plasma display panel according to claim 6, wherein the sustain-side bus electrodes and the scan-side bus electrodes have a thickness of 10 to 50 μ m, and the thickness of the sustain-side bus electrodes and the scan-side bus electrodes causes a raised portion of

thickness 2 to 50 μ m to be formed on the surface of an insulating layer provided on said front substrate.

21. (Amended) The plasma display panel according to claim 1, comprising a metal electrode connecting the sustain-side bus electrodes to each other.

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22. (Amended) The plasma display panel according to claim 1, comprising a transparent electrode connecting the sustain-side bus electrodes to each other.

23. (Amended) The plasma display panel according to claim 1, wherein the sustain-side bus electrodes are connected to each other to act as an integrated common bus electrode.

25. (Amended) The plasma display panel according to claim 23, wherein the common bus electrode has a thickness of 10 to 50 μ m, and the thickness of the common bus electrode causes a raised portion of thickness 2 to 50 μ m to be formed on the surface of an insulating layer provided on said front substrate.

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26. (Amended) The plasma display panel according to claim 1, comprising a metal electrode connecting the scan-side bus electrodes to each other.

27. (Amended) The plasma display panel according to claim 1, comprising a transparent electrode connecting the scan-side bus electrodes to each other.

28. (Amended) The plasma display panel according to claim 1, wherein the scan-side bus electrodes are connected to each other to act as an integrated common bus electrode.

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30. (Amended) The plasma display panel according to claim 28, wherein the common bus electrode has a thickness of 10 to 50 μ m, and the thickness of the common bus electrode causes a raised portion of thickness 2 to 50 μ m to be formed on the surface of an insulating layer provided on said front substrate.

37. (Amended) The plasma display panel according to claim 1, wherein the plane discharge electrodes are constructed so as to allow pixel cells disposed in the longitudinal column direction to have centers of light emission at equal intervals.

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38. (Amended) The plasma display panel according to claim 1, comprising horizontal black stripes disposed in the row direction.

40. (Amended) The plasma display panel according to claim 38, wherein the horizontal black stripes overlap neighboring scan-side bus electrodes in the column direction, and wherein

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the horizontal black stripes and a common bus electrode have the same width and are disposed at equal intervals in the column direction.

41. (Amended) The plasma display panel according to claim 38, wherein said scan electrodes and sustain electrodes are formed on said first substrate, and said horizontal black stripes are formed on the scan electrode and the sustain electrode.

42. (Amended) The plasma display panel according to claim 41, wherein a hole or notch is formed on the horizontal black stripes to ensure electrical connection of the scan electrode or the sustain electrode to the bus electrode portion.

Please add the following new claims:

43. (New) The plasma display panel according to claim 1, wherein the display electrode portion extends longitudinally in the column direction and the bus electrode portion extends longitudinally in the row direction.

44. (New) The plasma display panel according to claim 1, wherein each pixel cell comprises a sustain electrode, a sustain-side bus electrode, a scan electrode, and a scan-side bus electrode.

45. (New) The plasma display panel according to claim 1, wherein the sustain electrodes and the scan electrodes are disposed so as to allow respective sustain electrodes and scan electrodes to be adjacent to each other between neighboring pixel cells in the column direction.

46. (New) A display panel comprising:

a first substrate;

a second substrate provided opposite said front substrate;

a display electrode portion provided on said first substrate and extending longitudinally in a first direction; and

a bus electrode portion extending longitudinally in a second direction perpendicular to the first direction.

47. (New) The display panel according to claim 46, further comprising a plurality of electrodes provided on said second substrate and extending longitudinally in the first direction.

48. (New) The display panel according to claim 46, wherein the display electrode portion comprises a sustain electrode and a scan electrode, and the bus electrode portion comprises a sustain-side bus electrode and a scan-side bus electrode.

49. (New) The display panel according to claim 48, further comprising ribs which define pixel cells in the first direction and the second direction, respectively, to thereby define the pixel cells in a matrix.

50. (New) The display panel according to claim 49, wherein the sustain electrode is provided in common for pixel cells adjacent to each other in the first direction.

51. (New) The display panel according to claim 49, wherein a gap is formed in the first direction within each pixel cell between the sustain electrode and the scan electrode.

52. (New) An AC plane discharge plasma display panel comprising:
a front substrate;
a rear substrate;
a sealing portion for encapsulating said front substrate and said rear substrate at a peripheral edge portion thereof to seal a discharge gas therein;
column ribs and row ribs for defining pixel cells in a column direction and in a row direction, respectively, to thereby define the pixel cells in a matrix; and
plane discharge electrodes having a display electrode portion and a bus electrode portion, at least part of the display electrode portion of said plane discharge electrodes having a notched portion or a cut-away portion between pixel cells adjacent to each other in the row direction, said plane discharge electrodes having a pair of a sustain electrode and a scan electrode placed in one

Amendment Under 37 C.F.R. § 1.111

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pixel cell, and for neighboring pixel cells arranged in the column direction, sustain electrodes and scan electrodes are disposed to allow respective sustain electrodes and scan electrodes to be adjacent to each other between neighboring pixel cells.
